SRINIVAS INSTITUTE OF TECHNOLOGY, MANGALURU NUMBER OF SANCTIONED POST FOR THE YEAR 2017-18

SR.No.	Level of course	Name of the Course	Shift	Year Started	2013-14	2014-15	2015-16	2016-17	2017-18	Faculty
	DOCT CDADUATE	MASTERS IN BUSINESS ADMINISTRATION	1st Shift	2007				120	120	16
1	POST GRADUATE	MASTERS IN COMPUTER APPLICATIONS	1st Shift	2008			120	120	60	20
2	POST GRADUATE	INDUSTRIAL AUTOMATION AND ROBOTICS	1st Shift	2010				18	18	3
3	POST GRADUATE	DIGITAL ELECTRONICS	1st Shift	2010				24	18	3.5
4	POST GRADUATE	COMPUTER SCEINCE & ENGINEERING	1st Shift	2011				24	18	3.5
5	POST GRADUATE	THERMAL ENGINEERING	1st Shift	2011				18	0	1.5
6	POST GRADUATE	VLSI DESIGN AND EMBEDDED SYSTEMS	1st Shift	2012				18	0	1.5
7	POST GRADUATE		1st Shift	2012				36	0	3
8	POST GRADUATE	COMPUTER NETWORKING	2nd Shift	2012				60	0	4
9	POST GRADUATE	MASTERS IN BUSINESS ADMINISTRATION	Zila Ollik	2012						
1	UNDER GRADUATE	MECHANICAL ENGINEERING	1st Shift	2006		180	180	180	180	48
	UNDER GRADUATE	COMPUTER SCIENCE AND ENGINEERING	1st Shift	2006		120	120	120	120	32
3	UNDER GRADUATE	ELECTRONICS & COMMUNICATION ENGG	1st Shift	2006		120	120	120	120	32
4	UNDER GRADUATE	INFORMATION SCIENCE AND ENGINEERING	1st Shift	2006		60	60	60	60	16
	UNDER GRADUATE	COMPUTER SCIENCE AND ENGINEERING	2nd Shift	2010		60	60	60	60	16
5	UNDER GRADUATE	MECHANICAL ENGINEERING	2nd Shift	2010	_	60	60	60	60	16
7	UNDER GRADUATE	ELECTRICAL AND ELECTRONICS ENGINEERING	1st Shift	2011		60	60	60	60	16
	UNDER GRADUATE	AERONAUTICAL ENGINEERING	1st Shift	2011		60	60	60	60	16
8	UNDER GRADUATE	ELECTRONICS & COMMUNICATION ENGG	2nd Shift	2011		60	60	60	0	12
9	UNDER GRADUATE	MARINE ENGINEERING	1st Shift	2012	, t.	60	60	60	60	16
10	UNDER GRADUATE	AUTOMOBILE ENGINEERING	1st Shift	2012		120	120	120	60	28
11	UNDER GRADUATE	NANO TECHNOLOGY	1st Shift	2013		60	60	60	60	16
12	UNDER GRADUATE	INANO ILONNOLOGI								
1	UNDER GRADUATE	ARCHITECTURE	1st Shift	2012	80	80	80	80	80	25
	1 UNDER GRADUATE ARCHITECTURE TOTAL FAUCLTY									345

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SRINIVAS INSTITUTE OF TECHNOLOGY, MANGALURU NUMBER OF SANCTIONED POST FOR THE YEAR 2016-17

SR.No.	Level of course	Name of the Course	Shift	Year Started	2012-13	2013-14	2014-15	2015-16	2016-17	Faculty
1	POST GRADUATE	MASTERS IN BUSINESS ADMINISTRATION	1st Shift	2007				120	120	16
2	POST GRADUATE	MASTERS IN COMPUTER APPLICATIONS	1st Shift	2008			120	120	120	24
3	POST GRADUATE	INDUSTRIAL AUTOMATION AND ROBOTICS	1st Shift	2010				18	18	3
4	POST GRADUATE	DIGITAL ELECTRONICS	1st Shift	2010				24	24	4
5	POST GRADUATE	COMPUTER SCEINCE & ENGINEERING	1st Shift	2011				24	24	4
6	POST GRADUATE	THERMAL ENGINEERING	1st Shift	2011				18	18	3
7	POST GRADUATE	VLSI DESIGN AND EMBEDDED SYSTEMS	1st Shift	2012				18	18	3
8	POST GRADUATE	COMPUTER NETWORKING	1st Shift	2012				36	36	6
9	POST GRADUATE	MASTERS IN BUSINESS ADMINISTRATION	2nd Shift	2012				60	60	8
1	UNDER GRADUATE	MECHANICAL ENGINEERING	1st Shift	2006		180	180	180	180	48
2	UNDER GRADUATE	COMPUTER SCIENCE AND ENGINEERING	1st Shift	2006		120	120	120	120	32
3	UNDER GRADUATE	ELECTRONICS & COMMUNICATION ENGG	1st Shift	2006		120	120	120	120	32
4	UNDER GRADUATE	INFORMATION SCIENCE AND ENGINEERING	1st Shift	2006		60	60	60	60	16
5	UNDER GRADUATE	COMPUTER SCIENCE AND ENGINEERING	2nd Shift	2010		60	60	60	60	16
6	UNDER GRADUATE	MECHANICAL ENGINEERING	2nd Shift	2010		60	60	60	60	16
7	UNDER GRADUATE	ELECTRICAL AND ELECTRONICS ENGINEERING	1st Shift	2011		60	60	60	60	16
8	UNDER GRADUATE	AERONAUTICAL ENGINEERING	1st Shift	2011		60	60	60	60	16
9	UNDER GRADUATE	ELECTRONICS & COMMUNICATION ENGG	2nd Shift	2011		60	60	60	60	16
10	UNDER GRADUATE	MARINE ENGINEERING	1st Shift	2012		60	60	60	60	16
11	UNDER GRADUATE	AUTOMOBILE ENGINEERING	1st Shift	2012		60	120	120	120	28
12	UNDER GRADUATE	NANO TECHNOLOGY	1st Shift	2013		60	60	60	60	16
1	UNDER GRADUATE	ARCHITECTURE	1st Shift	2012	40	80	80	80	80	36
		TOTAL FAC	ULTY				•	Λ		375

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SRINIVAS INSTITUTE OF TECHNOLOGY, MANGALURU NUMBER OF SANCTIONED POST FOR THE YEAR 2015-16

SR.No.	Level of course	Name of the Course	Shift	Year Started	2012-13	2013-14	2014-15	2015-16	Faculty
1	POST GRADUATE	MASTERS IN BUSINESS ADMINISTRATION	1st Shift	2007			120	120	16
2	POST GRADUATE	MASTERS IN COMPUTER APPLICATIONS	1st Shift	2008		120	120	120	24
3	POST GRADUATE	INDUSTRIAL AUTOMATION AND ROBOTICS	1st Shift	2010			18	18	3
4	POST GRADUATE	DIGITAL ELECTRONICS	1st Shift	2010			24	24	4
5	POST GRADUATE	COMPUTER SCEINCE & ENGINEERING	1st Shift	2011			24	24	4
6	POST GRADUATE	THERMAL ENGINEERING	1st Shift	2011			18	18	3
7	POST GRADUATE	VLSI DESIGN AND EMBEDDED SYSTEMS	1st Shift	2012			18	18	3
8	POST GRADUATE	COMPUTER NETWORKING	1st Shift	2012			36	36	6
9	POST GRADUATE	MASTERS IN BUSINESS ADMINISTRATION	2nd Shift	2012			60	60	8
1	UNDER GRADUATE	MECHANICAL ENGINEERING	1st Shift	2006	120	180	180	180	44
2	UNDER GRADUATE	COMPUTER SCIENCE AND ENGINEERING	1st Shift	2006	120	120	120	120	32
3	UNDER GRADUATE	ELECTRONICS & COMMUNICATION ENGG	1st Shift	2006	120	120	120	120	32
4	UNDER GRADUATE	INFORMATION SCIENCE AND ENGINEERING	1st Shift	2006	60	60	60	60	16
5	UNDER GRADUATE	COMPUTER SCIENCE AND ENGINEERING	2nd Shift	2010	60	60	60	60	16
6	UNDER GRADUATE	MECHANICAL ENGINEERING	2nd Shift	2010	60	60	60	60	16
7	UNDER GRADUATE	ELECTRICAL AND ELECTRONICS ENGINEERING	1st Shift	2011	60	60	60	60	16
8	UNDER GRADUATE	AERONAUTICAL ENGINEERING	1st Shift	2011	60	60	60	60	16
9	UNDER GRADUATE	ELECTRONICS & COMMUNICATION ENGG	2nd Shift	2011	60	60	60	60	16
10	UNDER GRADUATE	MARINE ENGINEERING	1st Shift	2012	60	60	60	60	16
11	UNDER GRADUATE	AUTOMOBILE ENGINEERING	1st Shift	2012	60	60	120	120	24
12	UNDER GRADUATE	NANO TECHNOLOGY	1st Shift	2013		60	60	60	12
									0
1	UNDER GRADUATE	ARCHITECTURE	1st Shift	2012	40	80	80	80	28
		TOTAL FACULT	Υ				1		355

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SRINIVAS INSTITUTE OF TECHNOLOGY, MANGALURU NUMBER OF SANCTIONED POST FOR THE YEAR 2014-15

SR.No.	Level of course	Name of the Course	Shift	Year Started	2011-12	2012-13	2013-14	2014-15	Faculty
1	POST GRADUATE	MASTERS IN BUSINESS ADMINISTRATION	1st Shift	2007			120	120	16
2	POST GRADUATE	MASTERS IN COMPUTER APPLICATIONS	1st Shift	2008		120	120	120	24
3	POST GRADUATE	INDUSTRIAL AUTOMATION AND ROBOTICS	1st Shift	2010			18	18	3
4	POST GRADUATE	DIGITAL ELECTRONICS	1st Shift	2010			18	24	3.5
5	POST GRADUATE	COMPUTER SCEINCE & ENGINEERING	₱st Shift	2011			24	24	4
6	POST GRADUATE	THERMAL ENGINEERING	1st Shift	2011			18	18	3
7	POST GRADUATE	VLSI DESIGN AND EMBEDDED SYSTEMS	1st Shift	2012			18	18	3
8	POST GRADUATE	COMPUTER NETWORKING	1st Shift	2012			24	36	5
9	POST GRADUATE	MASTERS IN BUSINESS ADMINISTRATION	2nd Shift	2012			60	60	8
3	1 001 010 007112	*							
1	UNDER GRADUATE	MECHANICAL ENGINEERING	1st Shift	2006	120	120	180	180	40
2	UNDER GRADUATE	COMPUTER SCIENCE AND ENGINEERING	1st Shift	2006	120	120	120	120	32
3	UNDER GRADUATE	ELECTRONICS & COMMUNICATION ENGG	1st Shift	2006	120	120	120	120	32
4	UNDER GRADUATE	INFORMATION SCIENCE AND ENGINEERING	1st Shift	2006	60	60	60	60	16
5	UNDER GRADUATE	COMPUTER SCIENCE AND ENGINEERING	2nd Shift	2010	60	60	60	60	16
6	UNDER GRADUATE	MECHANICAL ENGINEERING	2nd Shift	2010	60	60	60	60	16
7	UNDER GRADUATE	ELECTRICAL AND ELECTRONICS ENGINEERING	1st Shift	2011	60	60	60	60	16
8	UNDER GRADUATE	AERONAUTICAL ENGINEERING	1st Shift	2011	60	60	60	60	16
9	UNDER GRADUATE	ELECTRONICS & COMMUNICATION ENGG	2nd Shift	2011	60	60	60	60	16
10	UNDER GRADUATE	MARINE ENGINEERING	1st Shift	2012		60	60	60	12
11	UNDER GRADUATE	AUTOMOBILE ENGINEERING	1st Shift	2012		60	60	120	16
12	UNDER GRADUATE	NANO TECHNOLOGY	1st Shift	2013			60	60	8
12									0
1	UNDER GRADUATE	ARCHITECTURE	1st Shift	2012		40	80	80	20
<u> </u>	1	TOTAL FAUCL	ГҮ						325.5

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Farangipete Post, Mangaluru-574 143

SRINIVAS INSTITUTE OF TECHNOLOGY, MANGALURU NUMBER OF SANCTIONED POST FOR THE YEAR 2013-14

SR.No.	Level of course	Name of the Course	Shift	Year Started	2010-11	2011-12	2012-13	2013-14	Faculty
1	POST GRADUATE	MASTERS IN BUSINESS ADMINISTRATION	1st Shift	2007			120	120	16
2	POST GRADUATE	MASTERS IN COMPUTER APPLICATIONS	1st Shift	2008		60	120	120	16
3	POST GRADUATE	INDUSTRIAL AUTOMATION AND ROBOTICS	1st Shift	2010			18	18	3
4	POST GRADUATE	DIGITAL ELECTRONICS	1st Shift	2010			18	18	3
5	POST GRADUATE	COMPUTER SCEINCE & ENGINEERING	1st Shift	2011			18	24	3.5
6	POST GRADUATE	THERMAL ENGINEERING	1st Shift	2011			18	18	3
7	POST GRADUATE	VLSI DESIGN AND EMBEDDED SYSTEMS	1st Shift	2012			18	18	3
8	POST GRADUATE	COMPUTER NETWORKING	1st Shift	2012			24	24	4
9	POST GRADUATE	MASTERS IN BUSINESS ADMINISTRATION	2nd Shift	2012			60	60	8
1	UNDER GRADUATE	MECHANICAL ENGINEERING	1st Shift	2006	120	120	120	180	36
2	UNDER GRADUATE	COMPUTER SCIENCE AND ENGINEERING	1st Shift	2006	120	120	120	120	32
3	UNDER GRADUATE	ELECTRONICS & COMMUNICATION ENGG	1st Shift	2006	120	120	120	120	32
4	UNDER GRADUATE	INFORMATION SCIENCE AND ENGINEERING	1st Shift	2006	60	60	60	60	16
5	UNDER GRADUATE	COMPUTER SCIENCE AND ENGINEERING	2nd Shift	2010	60	60	60	60	16
6	UNDER GRADUATE	MECHANICAL ENGINEERING	2nd Shift	2010	60	60	60	60	16
7	UNDER GRADUATE	ELECTRICAL AND ELECTRONICS ENGINEERING	1st Shift	2011		60	60	60	12
8	UNDER GRADUATE	AERONAUTICAL ENGINEERING	1st Shift	2011	11	60	60	60	12
9	UNDER GRADUATE	ELECTRONICS & COMMUNICATION ENGG	2nd Shift	2011		60	60	60	12
10	UNDER GRADUATE	MARINE ENGINEERING	1st Shift	2012			60	60	8
11	UNDER GRADUATE	AUTOMOBILE ENGINEERING	1st Shift	2012			60	60	8
12	UNDER GRADUATE	NANO TECHNOLOGY	1st Shift	2013				60	4
1	UNDER GRADUATE	ARCHITECTURE	1st Shift	2012			40	80	12
- 1	JOHN CHANGONIE	TOTAL FACULT	Υ			•			275.5

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