**Ms.Vijayalatha Devadiga.**

The primary aim of research centre is to enhance the knowledge in the field of signal/ Image processing, VLSI architecture and other advanced method to provide the application to the society.

**Post Graduate Projects:**

➢ Audio steganography by LSB algorithm and encryption key with elliptic curve cryptography.

➢ An efficient dynamic image compression algorithm based on block optimization, byte compression and Run length encoding along Y-axis.

➢ Investigating a junction based multipath source routing algorithm for vanets.

➢ Automatic Image targeting and enhancement using content aware based seam carving technique.

➢ Low power, leakage reduction 7T-SRAM cell using SVL scheme.

➢ Adaptive edge enhanced color image processor using VLSI implantation.

➢ A novel LDPC coded MIMO-OFDM architecture for the next generation communication system.

➢ Design and analysis of multiplexer based physical unclonable functions.

➢ A 6-bit Hybrid architecture current–steering DAC.

**Under Graduate Projects :**

➢ Multi tasking Robot for Disaster Management(SPP-2018, Sponsored by KSCST, Bengaluru

➢ Line follower Robot.

➢ Data hiding in a video.

➢ Wireless Solar Based Robotic vehicle for garbage separation process.

➢ Automatic ration material distribution system.

**Paper Published:**

Presented Paper on " Image compression  algorithm using BOBC and RLE"  in the  National conference NCARE-2014 at RJLIT, Chikkaballapur.